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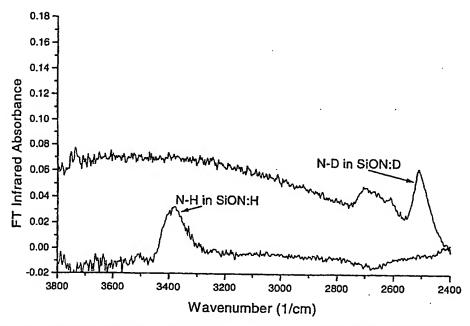
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(54) Title: MATERIAL FOR SION-OPTICAL WAVEGUIDES AND METHOD FOR FABRICATING SUCH WAVEGUIDES



(57) Abstract: A new material is provided that can be used for the fabrication of planar optical waveguides. The material includes silicon, oxygen and nitrogen and additionally deuterium. Also provided is a method for fabricating planar optical waveguides based on this new material, which uses deuterated gaseous precursors.

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#### DESCRIPTION

### Material for SiON-Optical Waveguides and Method for Fabricating such Waveguides

#### Field of the Invention

10 The present invention relates to a material which can be used in the fabrication of optical waveguides. More specifically, the invention deals with such a material that includes silicon, oxygen and nitrogen. Still more specifically, the present invention pertains to a method for fabricating 15 waveguides including such material.

#### Background of the Invention, Prior Art

20 High-bandwidth communication today relies increasingly on optical fibers and optical components for signal enhancement, signal-routing, adding and dropping information. The requirement for high capacity communication systems has led to the use of optical waveguides. An optical waveguide consists of a core made of some high-refractive index material and a cladding of a low-refractive-index material surrounding the core. In traditional optical-waveguide technology, the core and cladding are made of doped silica glass, where the refractive index contrast, i.e., the difference of the 30 refractive index of the core layer relative to the cladding layers, is achieved by a doping profile, e.g., using P-doping or Ge-doping. In order to fabricate optical components in a cost-effective way in mass production, it is desirable to

increase the areal density. This can be achieved with the SiON

35 technology. A review on planar waveguide technology is given,

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e.g., in "Silica-based optical integrated circuits" by Y.P. Li and C.H. Henry; IEEE Proc. - Optoelectron., Vol. 143, No. 5, October 1996, pp. 263-280.

- 5 In order to increase the areal density compared to traditional technology, smaller bending radii are required. Smaller bending radii, however, require a stronger guiding of the optical modes than in a straight waveguide or fiber. This is achieved by increasing the refractive index contrast between
- 10 core and cladding. An example for a method for increasing the index of refraction of a glassy material is given in US-A-5,500,031, wherein the material is treated with hydrogen under the application of heat. However, the maximum doping levels with P, B, Ge or other dopants in SiO<sub>2</sub> (silica) limit the
- 15 maximum achievable index contrast and hence a minimal attainable bending radius in the waveguide exists. In both conventional fiber and planar Ge-silica (SiO<sub>2</sub>) there is an index contrast ranging from 0.002 up to 0.006. This allows a curvature radius—of typically—15—mm. The index-contrast—in
  - 20 SiON technology is typically 0.02 (i.e., 3x to 10x higher) and allows for a curvature radius down to 1.5 mm. This corresponds to an increase in areal density of 100x. A good compromise between the minimum bending radius and coupling losses to the standard fiber and the necessary difference in the material
  - 25 composition between core and cladding resulting therefrom can be obtained, e.g., with an effective refractive-index contrast around 0.02.

If the cladding is made of silica, i.e., SiO<sub>2</sub>, which has a refractive index of 1.45, a material having a refractive index near 1.51 is desired for the core. Waveguides with such a high index contrast can be fabricated with silicon-oxinitride (SiON) core layers. An example for the use of SiON as a material for fabricating waveguides is given in 35 US-A-5,416,861.

Owing to the nitrogen "doped" silicon-oxide compound, the refractive index can be adjusted in a very flexible way to match other design criteria such as, e.g., achievable 5 dimensions. A relatively high effective lateral contrast in refractive index can be used which is approximately 10 x higher than that used for Ge-doped SiO2 waveguides. This allows a 10 x smaller radius of curvature (1.5 mm) to be used in circuit layouts.

10

A typical fabrication technique is to deposit silicon oxinitride by means of a PECVD process using silane (SiH4), nitrous-oxide ( $N_2O$ ), and ammonia ( $NH_3$ ) as gaseous precursors. The resulting material, however, has a large hydrogen 15 concentration. Hydrogen is incorporated in the form of hydroxyl groups, Si-H groups and NH- and NH2-fragments. These groups and fragments introduce additional absorption into the optical transmission characteristic of the silicon oxinitride. The first overtone of the NH-induced absorption lies at 1510 20 nm and overlaps with the spectral window which is used for optical signal transmission and which extents from 1540 nm to 1570 nm, hereinafter simply referred to as the optical transmission window. This window has been chosen for optical transmission due to the fact that the optical transmission 25 losses are minimal around 1550 nm (due to purity of fabrication) and that for all-optical amplifiers in fiber networks (avoiding electrical-optical conversions) the only currently available amplifiers are based on Er (Erbium) -doping of SiO<sub>2</sub> fibers and the resultant amplification window is 30 limited to 1540-1570 nm. Therefore, the accepted standard for the optical telecommunication frequencies has been assigned to this window (cf. "Review of Rare Earth Doped Fiber Lasers and

Amplifiers" by P. Urquhart; IEEE Proc., Vol. 135, Pt. J, No.

6, December 1988, pp. 385-407. 35

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The above mentioned NH-absorption results in often unacceptably large losses for viable products. The hydrogen concentration and with it the resulting absorption losses are therefore currently reduced by a high-temperature annealing 5 process as described, e.g., in MRS Proceedings Book of the 1999 Spring Meeting of the MRS Materials Research Society, "Materials Research Society (MRS) Spring Meeting 1999, 5-8 April 1999, Paper BB.8.3, Symposium BB on Multicomponent Oxides", MRS Proceedings Volume 574, pp. 255-260, 1999 (ISBN 10 1-55899-481-5). The process consolidates the SiON material, and removes hydrogen from the N-H bond in the form of  ${\rm H}_2$  and  $H_2O$ . In conventional SiON material (SiON:H), the losses are reduced to lower values by higher anneal temperatures or longer anneal durations, but both routes bear the risk of 15 introducing unwanted defect-scattering or crystallization. A further negative side effect of the high-temperature annealing is the introduction of anisotropic stress, caused by the difference in thermal expansion coefficients between the silicon-substrate-and-the-material-of-the-waveguide. The 20 stress correlates with the temperature difference between room temperature and the highest annealing temperature or a

temperature and the highest annealing temperature or a material-specific temperature close to its annealing point, whichever is lower. The stress gives rise to birefringence and polarization-dependent transmission characteristics and is not desired for most applications.

It is known in the art of fiber manufacturing to use deuterium in place of hydrogen to produce glass having a low hydroxyl group (-OH) content. The hydroxyl group normally present in 30 glass produced in the presence of hydrogen is replaced by the deuterium containing ion OD. Absorption peaks normally caused by the presence of the hydroxyl ions are shifted to longer wavelengths at which the absorptions are not troublesome when the waveguide is used to transmit light in the band of approximately 7,000 Å. Thus, US-A-3,791,714 discloses the

production of a SiO<sub>2</sub>-waveguide by a flame hydrolysis process using deuterium gas or a deuterium compound gas which is passed through a liquid containing a silicon compound such as silicon tetrachloride. The resulting vapor is burned at high 5 temperatures (about 1800°C) to deposit a film of silicon dioxide on a rotating mandrel, resulting in a "soot" deposit that contains OD-bonds instead of the usual OH-groups.

US-A-5,872,387 discloses semiconductor devices, including MOS devices, that are treated with deuterium to improve their operational characteristics. The process comprises a step of passivating the device with deuterium by disposing molecular, atomic or ionic deuterium in the areas of the device in which protection against hot carrier effects is desired. The 15 deuterium is covalently bonded with atoms in this area so as to be stable incorporated.

A plate plastics optical waveguide is described in US-A-5,062,680 which includes a core portion composed of a polymer 20 containing a deuterium atom or a halogen atom, and a clad portion surrounding the core portion, the clad portion being composed of a polymer having a refractive index lower than that of the core portion.

25 While US-A-5,872,387 only describes a post-fabrication passivation in a heated, deuterium gas-enriched atmosphere, and US-A-5,062,680 uses a polymer containing deuterium to produce the optical waveguide, the main disadvantage of US-A-3,791,714 is the fact that the optical waveguide prepared according to the process given therein still contains hydroxyl groups and is only applicable to manufacture waveguides in the form of fibers.

Accordingly, there is still a need for slab optical waveguides based on SiON material having extreme small optical losses and 35 very low mechanical stress.

#### Summary of the Invention

It is an object of the present invention to provide silicon-5 oxinitride (SiON) layers for applications as planar optical waveguides having extreme low optical losses in the region of operation.

It is also an object of the present invention to provide a 10 low-temperature process to manufacture such planar optical waveguides.

It is a further object of the present invention to propose a technique to manufacture such waveguides in a low-cost, mass15 fabrication technology.

It is still a further object of the present invention to provide optical waveguides that show low mechanical stress and a significantly reduced stress-related birefringance.

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Still another object of the present invention is the provision of silicon-oxide based optical waveguides that could be integrated into conventional CMOS technology.

25 These and other objetcs and advantages are achieved by the material disclosed in claim 1 and the method claimed in claim 3.

Preferred embodiments of the invention are described in the 30 dependent claims.

To achieve the above mentioned objects, a new material for the manufacture of planar optical waveguides is proposed as set forth in independent claim 1.

#### Brief Description of the Drawings

The invention will be described in more detail hereinafter in connection with the drawings, in which

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- Fig. 1 is an infrared spectrum showing the NH-absorption band of conventional SiON:H material as compared to the SiON:D material according to the invention;
- 10 Fig. 2 shows the slab propagation loss of SiON:D as compared to the SiON:H material annealed according to the state of the art;
- Fig. 3 depicts the compressive stress as a function of temperature for state of the art material as compared to the material according to the invention; and
- Fig. 4 shows a plot of the estimated birefringence dependent on compressive stress.

#### Detailed Description of the Preferred Embodiment

25 Silicon-oxinitride (SiON) layers can function as planar optical waveguides in the 1550-nm wavelength region. The optically guiding SiON waveguide core layer has a relatively high refractive index of 1.500 and is sandwiched between two silicon- oxide cladding layers with a lower refractive index 30 of 1.450. The SiON layer is deposited by PECVD using silane, nitrous oxide and ammonia as gaseous precursors. Waveguide bends with a radius of curvature as small as 1.5 mm can be realized because of high refractive index difference

achievable between core and cladding layers. This allows the

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fabrication of compact, relatively complex integrated optical waveguide devices.

Owing to the hydrogenated precursors, the conventional, state 5 of the art SiON layers (in the following called SiON:H) contain a large number of N-H bonds with their intrinsic infrared absorption at a wave number of 3350 cm-1. The first overtone of this frequency is found at a wavelength of 1510 nm and its low-energy tail leads to an unwanted absorption loss 10 in the wavelength region of interest (1545 to 1565 nm). Fig. 1 shows a typical absorption spectrum of a state of the art planar slab waveguide structure with an as-deposited SiON core layer (SiON:H; lower curve). A typical loss for unannealed SiON:H material is 10 dB/cm at 1510 nm and 1.0 - 1.2 dB/cm at 15 1550 nm (a factor of 10x higher than annealed material). The N-H bonds can be largely removed by annealing the SiON layers in an atmospheric nitrogen flow at a temperature of approximately 1140°C. The peak losses at 1510 nm then decrease to a value of 1.0 dB/cm, and the losses between 1545 and 1565 20 nm decrease to typical values of 0.15 ± 0.05 dB/cm what is shown in Fig. 2.

However, this high-temperature annealing process is an expensive manufacturing step which, in addition, slows down 25 the wafer turnaround time. Moreover, stress build-up follows upon cooling down the relaxed layers from high-temperature, due to mismatched thermal expansion. The induced stress effects result in stress- related polarization dependence (birefringence). In addition to that, since the waveguide 30 fabrication is a "high-temperature" process, only special substrate materials can be used which are able to stand these high temperatures.

The present invention therefore proposes a "low-temperature"

35 process for the manufacture of planar slab optical waveguides

based on SiON material by using a deuterated SiON material (in the following called SiON:D). "Low temperature" here means that the foreseen maximum temperature of all process steps is in the range of 550 - 850°C, including densification steps and 5 tuning of mechanicak stress. A "densification window" of 550 -850°C might also be useful to stabilize the SiON:D properties. By using SiON:D material, the above mentioned anneal cycle at 1400°C (taking 22 hours) is avoided. Thus, the work-flow is simplified and a wider process latitude is given as the use of 10 a wider temperature window is allowed. Moreover, the proposed method offers the possibility to integrate the optical waveguides into a conventional CMOS process for further hybrid integration. In addition to that, low mechanical stress in the oxidic layer stack can be achieved, the estimated lowering of 15 compressive stress in SiON:D material is by a factor 3,5 from about -270 to -80 MPa (cf. Fig. 3). The typical value for SiON:H material is -120 MPa (as-grown) and -270 MPa (after 1145°C anneal). In contrast to that the value for SiON:D material according to the invention is -115 MPa (as grown), no 20 anneal is necessary. The calculated unannealed SiON:D value of -80 MPa shown in Fig. 3 is based on best estimates on the time the invention was made, the currently cited value is based on three SiON:D wafers with a value of -115 +/- 10 MPa. Thequoted stress values pertain to planar SiON:D and planar SiON:H 25 layers on SiO2 (lower cladding) base layers. For a full waveguide device, these SiON:X layers (X = H, D) are covered with a top-cladding SiO2 layer to form an oxidic tri-layer. This overgrowth of SiON:X has the effect of making the oxidic sandwich mechanically more symmetric and hence lowers the 30 residual stress and thus the stress-related birefringence.

An estimate of this birefringence is shown in Fig. 4, estimating that the SiON:X trilayer waveguide devices of the present invention will have zero-birefringence (or 35 polarization independent) at a compressive stress of -80 to

-100 MPa. It is also noted that an ultimative slight compressive stress (about -50 MPa) is probably advatageous for a multi-layered device in order to remain mechanically intact, because a lower or tensile stress value might easily lead to delamination, peeling off and structural insufficiency. Thus the SiON:D according to the present invention offers far better mechanically tuning opportunities than annealed SiON:H, where the loss-lowering procedure determines the temperature history and hence the residual stress.

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It has to be noted that the new method also offers the advantages already present when using annealed SiON:H material, namely eliminating N-H loss (cf. Fig. 2; lower curve), thus opening a wider wavelength window for SiON photonic ICs, realization of radius of curvature of as small as 1.5 mm, etc., associated with the high refractive index of SiON.

As compared to the state of the art processes already

- 20 mentioned before, the method according to the invention does not change the starting materials by replacing hydrogen with deuterium but starts already using deuterated gaseous precursors, namely SiD<sub>4</sub> and/or ND<sub>3</sub>. The work performed proves that the largest contribution to the conventional N-H loss at
- 25 1510 nm originates from the N-H precursor. Starting the process using ND<sub>3</sub> as a precursor lowers the loss to the annealed, conventional value of SiON:H. Using both hydrogenated presursors, SiD<sub>4</sub> and ND<sub>3</sub>, lowers the loss >5x over the conventionally annealed SiON:H material. It has to be
- 30 mentioned that only small deuterated gas-volumes are consumed and thus the process is very cost-effective. The present plasma deposition process consumes per full SiON:D core 4" wafer three liters of ND3 and 30 liters of 2% SiD4 (diluted in He), thsi implies about 330 full 4" SiON:D wafers from a
- 35 standard gas cylinder.

The incorporation of nitrogen, "N-doping", has a far stronger effect on refractive index than B-, P- or Ge-doping, because the N-doping continuously changes the host material, in 5 contrast to light additives as B, P or Ge. The effect of nitrogen on the refractive index is about 10x stronger and thus an adequate optical "bending" into a 1.5 mm radius is obtained at a nitrogen atomic content of 8.0 +/- 1 % N at a refractive index of 1.510. The preferred range of nitrogen is 10 about 3 to about 15 atomic %; the 3% lower limit gives some advantage in waveguide radius over conventional Ge:SiO2 and is easily detectable, the 15% as a high limit will give a radius of about 0.5 mm.

15 The preferred composition in relative atomic fractions [%] of the SiON:D material is as follows, with, e.g., the "N atomic fraction" being the amount of N atoms divided by the sum of the sum of Si, O and N atoms.

	Element	Fraction
	Si	10
	0	19
	N	0.05 - 0.10
	D	0.05 - 0.15
20	Н	< 0.01

As has been previously mentioned, either one or both precursors for the manufacture of SiON waveguides, namely NH<sub>3</sub> and SiH<sub>4</sub>, can be deuterated. Five precursor mixtures have been 25 used in the evaluation of infrared absorption and optical loss. The following table gives the improvement factors as compared to the standard-annealed hydrogenated SiON:H material (M1).

	Mixture	Improvement	Remarks
M1	$SiH_4/NH_3/N_2O$	<del></del>	Reference SiON
M2	$SiD_4/ND_3/N_2O$	5-7x	All deuterated
м3	SiH <sub>4</sub> /ND <sub>3</sub> /N <sub>2</sub> O	Marginal	SiH₄ and N₂O
M4·	$SiD_4//N_2O$	Marginal	Other Chemistry
M5	SiD <sub>4</sub> /NH <sub>3</sub> /N <sub>2</sub> O	None	Contains NH <sub>3</sub>

"Other chemistry" in M4 indicates there is not the usual plasma chemistry using SiH/NH/NO (or its D-isotopes), but 5 rather SiH/NO without NH; thus the chemical reaction or synthesis routes are different.

This indicates that by far the largest contribution of N-H in SiON:H originates from the NH<sub>3</sub> gas in the current  $SiH_4/NH_3/N_2O$  10 mixture.

Improvement in optical loss at 1510 nm by a factor 5 to 7 is obtained with the set M2-without annealing, compared to the standard SiON:H hydrogenated results after annealing. Loss figures in dB/cm are compared in the following table (cf. Also Fig. 2).

Process		Loss @ 1510 nm	Loss (	1540 nm
Standard	SiON:H	$1.0 \pm 0.2$	0.13	0.02
M2	SiON:D	0.16 ± 0.02	0.07	0.02

- 20 The method according to the invention comprises the following steps:
  - A) Oxidation of silicon wafer to a defined thickness by oxygen diffusion at high temperature;
- 25 B) Deposition on SiON waveguide layer by Plasma-CVD;
  - C) Definition of ridge structure via lithography and etching in the SiON core layer;

- D) Overgrowth of core/ridge structure ith SiO<sub>2</sub> by Plasma-CVD or by thermal CVD;
- E) Densification of the overgrowth layer to adjust birefringence; and
- 5 F) Dicing of wafer into individual chips and polishing of optical input/output facets of the individual devices.

Fig. 3 shows the compressive stress as a function of temperature. It can be seen that the material according to the 10 invention (SiON:D) has a significantly lower value of stress than the conventionally produced material (SiON:H).

In Fig. 4 there is provided a plot of the estimated dependence of birefringence on the compressive stress in a waveguide.

15 Given a value of about -80 MPa for the material according to the invention, it is clearly perceptible that the resulting birefringence is drastically decreased.

The outstanding advantage of the proposed method is the

20 complete elimination of N-H related absorption in SiON leading
to ultra- low loss material with a wide optical bandwidth. The
proposed technique is a low-cost, mass-fabrication technology
when implemented in PECVD for two reasons: (i) small
deuterated gas-volumes are consumed and (ii) expensive

25 manufacturing processing steps like high-temperature annealing
are eliminated. Thus, a faster wafer turnaround time is
obtained.

The avoidance of the anneal cycle simplifies the process

30 workflow and gives a wider process latitude as it allows the use of a wider temperature window, allowing the design of other cladding/sintering treatments (densification) to even further lower the stress-related effects. The new process latitude is used to deliberately optimize a moderate

35 temperature cycling for stress adjustment and for the first

time offers the possibility to integrate the optical waveguides into a conventional CMOS process for further hybrid integration. Applications could be optical clock distribution, optical receiver or power drivers for thermal tuning.

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#### CLAIMS

 A composition of matter comprising essentially silicon (Si), oxygen (O), and nitrogen (N), and additionally comprising deuterium (D) and hydrogen (H),

characterized in that

- the concentration in relative atomic fractions of D is from about 0.03 to about 0.20 and the concentration in relative atomic fractions of H is below 0.01.
  - The composition of matter of claim 1, wherein the concentration of D is from about 0.05 to about 0.15.

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- 3. The composition of matter according to claim 1 or 2, wherein the concentration of nitrogen is between 1 and 99 atomic-%.
- 20 4. The composition of matter according to claim 3, wherein the concentration of nitrogen is between 1 and 50 atomic-%, preferably between 3 and 15 atomic-%.
- 5. Method for fabricating planar optical waveguides based on silicon oxinitride (SiON) material by means of a PECVD process,

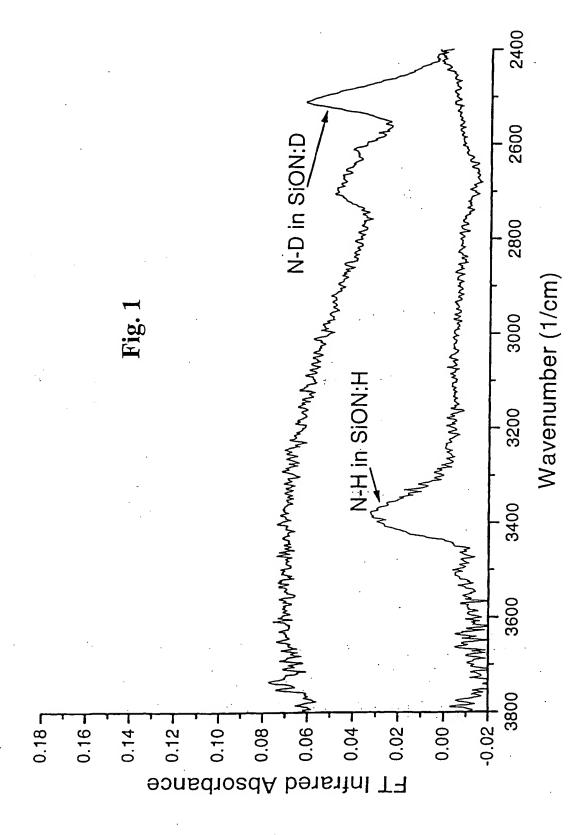
characterized in that

- deuterated gaseous precursors are used as starting materials.
- 6. Method according to claim 5, wherein the maximum temperature reached during the process steps is in the range of about 550 to about 850°C.

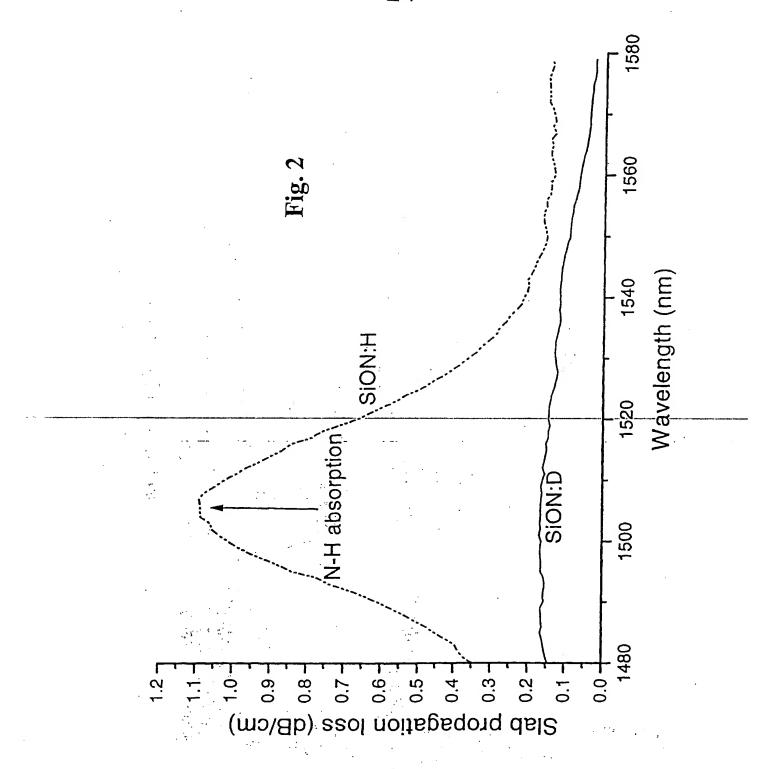
7. Method according to claim 6, wherein the higher value of the temperature range is only reached in case a densification of the obtained SiON material is necessary.

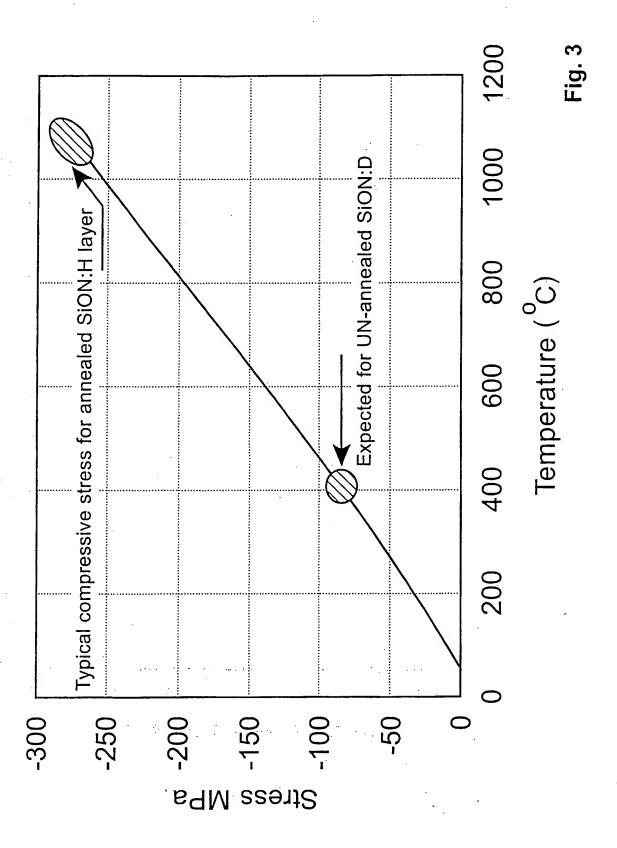
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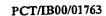
- 8. Method according to claim 5, wherein the deuterated gaseous precursors are  $SiD_4$  and  $ND_3$ .
- 9. Method according to claim 8, wherein only one of said deuterated precursors is used.
  - 10. Method according to claim 8, wherein both of the gaseous precursors are used.
- 15 11. Use of the material according to any one of claims 1 to 4 as optical waveguide material.



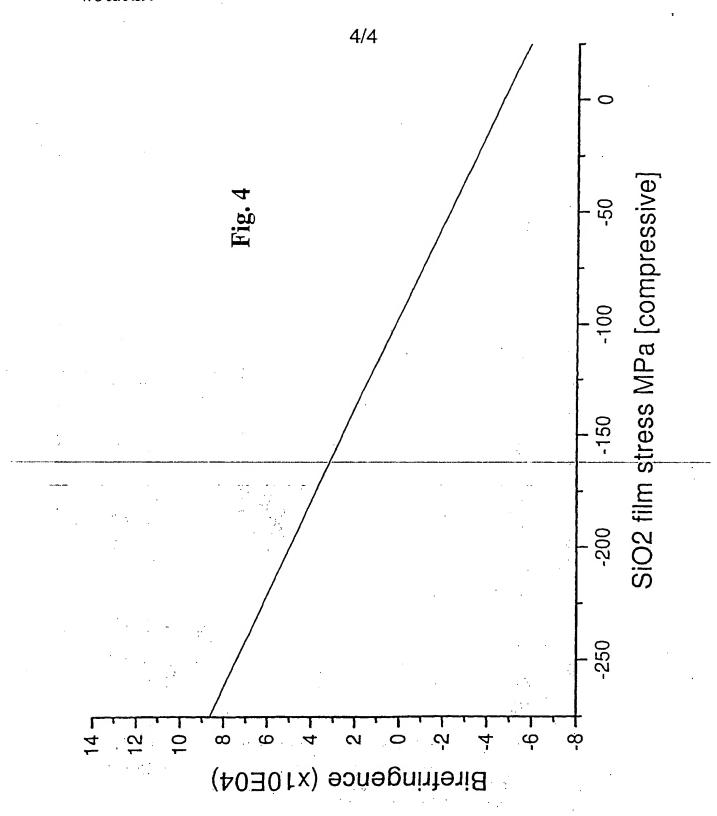
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